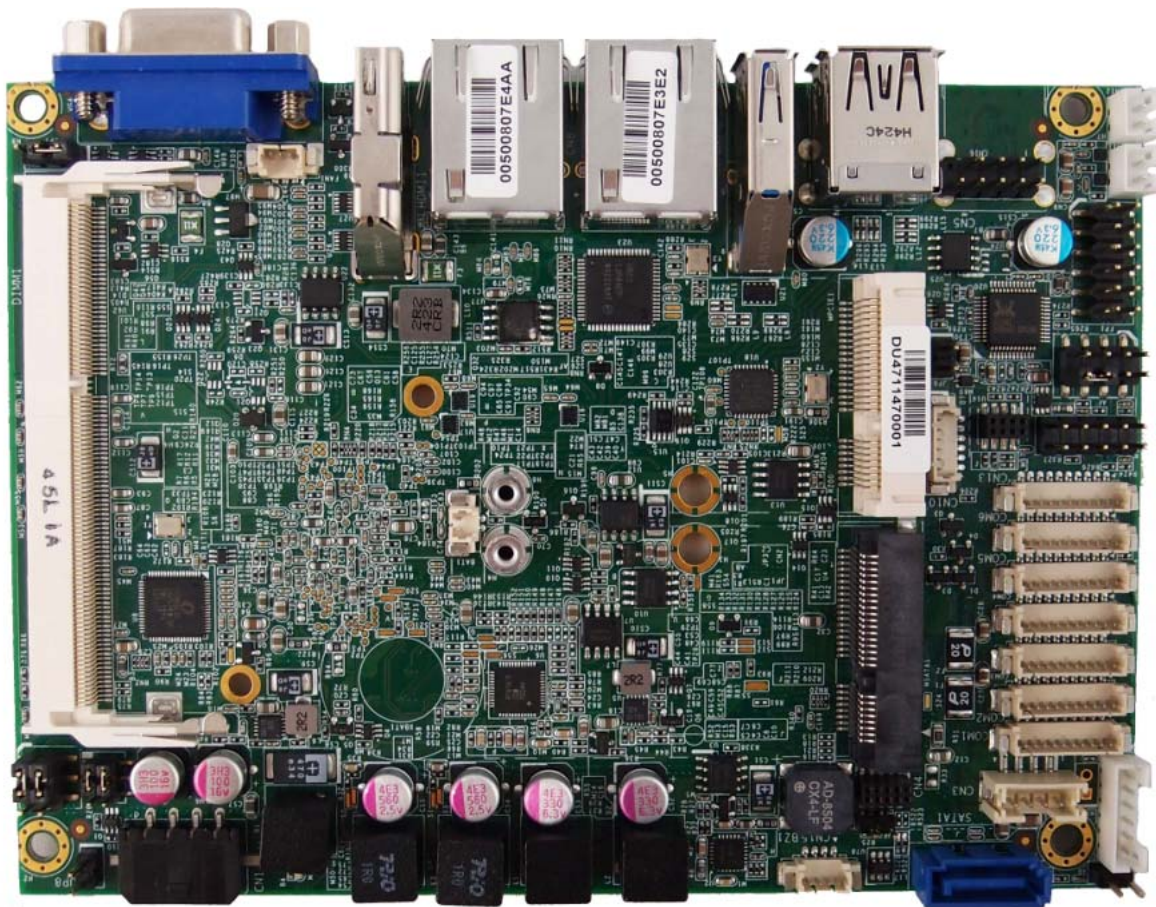


KEEX-1760 Series

Intel® Embedded Compact Extended Form Factor
with Intel® BayTrail Processors

User's Guide



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Quanmax reserves the right to make changes without notice in product or component design as warranted by evolution in user needs or progress in engineering or manufacturing technology.

Changes which affect the operation of the unit will be documented in the next revision of this user's guide.

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Safety Instructions

■ Before You Begin

Before handling the product, read the instructions and safety guidelines on the following pages to prevent damage to the product and to ensure your own personal safety. Refer to the “Advisories” section in the Preface for advisory conventions used in this user’s guide, including the distinction between Warnings, Cautions, Important Notes, and Notes.

- Always use caution when handling/operating a computer. Only qualified, experienced, authorized electronics service personnel should access the interior of a computer. The power supplies produce high voltages and energy hazards, which can cause bodily harm.
- Use extreme caution when installing or removing components. Refer to the installation instructions in this user’s guide for precautions and procedures. If you have any questions, please contact Quanmax Post-Sales Technical Support.

WARNING



High voltages are present inside the chassis when the unit’s power cord is plugged into an electrical outlet. Turn off system power, turn off the power supply, and then disconnect the power cord from its source before removing the chassis cover. Turning off the system power switch does not remove power to components.

■ When Working Inside a Computer

Before taking covers off a computer, perform the following steps:

1. Turn off the computer and any peripherals.
2. Disconnect the computer and peripherals from their power sources or subsystems to prevent electric shock or system board damage. This does not apply when hot swapping parts.

3. Follow the guidelines provided in “Preventing Electrostatic Discharge” on the following page.
4. Disconnect any telephone or telecommunications lines from the computer.

In addition, take note of these safety guidelines when appropriate:

- To help avoid possible damage to system boards, wait five seconds after turning off the computer before removing a component, removing a system board, or disconnecting a peripheral device from the computer.
- When you disconnect a cable, pull on its connector or on its strain-relief loop, not on the cable itself. Some cables have a connector with locking tabs. If you are disconnecting this type of cable, press in on the locking tabs before disconnecting the cable. As you pull connectors apart, keep them evenly aligned to avoid bending any connector pins. Also, before connecting a cable, make sure both connectors are correctly oriented and aligned.

CAUTION



Do not attempt to service the system yourself except as explained in this user's guide. Follow installation and troubleshooting instructions closely.

■ Preventing Electrostatic Discharge

Static electricity can harm system boards. Perform service at an ESD workstation and follow proper ESD procedure to reduce the risk of damage to components. Quanmax strongly encourages you to follow proper ESD procedure, which can include wrist straps and smocks, when servicing equipment. You can also take the following steps to prevent damage from electrostatic discharge (ESD):

- When unpacking a static-sensitive component from its shipping carton, do not remove the component's antistatic packing material until you are ready to install the component in a computer. Just before unwrapping the antistatic packaging, be sure you are at an ESD workstation or grounded. This will discharge any static electricity that may have built up in your body.

Safety Instructions

- When transporting a sensitive component, first place it in an antistatic container or packaging.
- Handle all sensitive components at an ESD workstation. If possible, use antistatic floor pads and workbench pads.
- Handle components and boards with care. Don't touch the components or contacts on a board. Hold a board by its edges or by its metal mounting bracket.
- Do not handle or store system boards near strong electrostatic, electromagnetic, magnetic, or radioactive fields.

Preface

■ How to Use This Guide

This guide is designed to be used as step-by-step instructions for installation, and as a reference for operation, troubleshooting, and upgrades.

NOTE



Driver downloads and additional information are available under Downloads on our web site: www.quanmax.com.

■ Unpacking

When unpacking, follow these steps:

1. After opening the box, save it and the packing material for possible future shipment.
2. Remove all items from the box. If any items listed on the purchase order are missing, notify Quanmax customer service immediately.
3. Inspect the product for damage. If there is damage, notify Quanmax customer service immediately. Refer to “Warranty Policy” for the return procedure.

■ Regulatory Compliance Statements

This section provides the FCC compliance statement for Class A devices.

FCC Compliance Statement for Class A Devices

The product(s) described in this user's guide has been tested and found to comply with the limits for a Class A digital device, pursuant to Part 15 of the FCC Rules.

These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the user's guide, may cause harmful interference to radio communications. Operation of this equipment in a residential

area (domestic environment) is likely to cause harmful interference, in which case the user will be required to correct the interference (take adequate measures) at their own expense.

Changes or modifications not expressly approved by Quanmax could void the user's authority to operate the equipment.

NOTE



The assembler of a personal computer system may be required to test the system and/or make necessary modifications if a system is found to cause harmful interference or to be noncompliant with the appropriate standards for its intended use.

■ Warranty Policy

Limited Warranty

Quanmax Inc.'s detailed Limited Warranty policy can be found under Support at www.quanmax.com. Please consult your distributor for warranty verification.

The limited warranty is void if the product has been subjected to alteration, neglect, misuse, or abuse; if any repairs have been attempted by anyone other than Quanmax or its authorized agent; or if the failure is caused by accident, acts of God, or other causes beyond the control of Quanmax or the manufacturer. Neglect, misuse, and abuse shall include any installation, operation, or maintenance of the product other than in accordance with the user's guide.

No agent, dealer, distributor, service company, or other party is authorized to change, modify, or extend the terms of this Limited Warranty in any manner whatsoever.

Quanmax reserves the right to make changes or improvements in any product without incurring any obligation to similarly alter products previously purchased.

Return Procedure

For any Limited Warranty return, please contact Support at www.quanmax.com and login to obtain a Return Material Authorization (RMA) Number. If you do not have an account, send an email to support@quanmax.com to apply for one.

All product(s) returned to Quanmax for service or credit must be accompanied by a Return Material Authorization (RMA) Number. Freight on all returned items must be prepaid by the customer who is responsible for any loss or damage caused by common carrier in transit. Returns for Warranty must include a Failure Report for each unit, by serial number(s), as well as a copy of the original invoice showing the

date of purchase.

To reduce risk of damage, returns of product must be in a Quanmax shipping container. If the original container has been lost or damaged, new shipping containers may be obtained from Quanmax Customer Service at a nominal cost. Quanmax owns all parts removed from repaired products. Quanmax uses new and reconditioned parts made by various manufacturers in performing warranty repairs and building replacement products. If Quanmax repairs or replaces a product, its warranty term is not extended.

Shipments not in compliance with this Limited Warranty Return Policy will not be accepted by Quanmax.

Limitation of Liability

In no event shall Quanmax be liable for any defect in hardware, software, loss, or inadequacy of data of any kind, or for any direct, indirect, incidental, or consequential damages in connection with or arising out of the performance or use of any product furnished hereunder. Quanmax's liability shall in no event exceed the purchase price of the product purchased hereunder. The foregoing limitation of liability shall be equally applicable to any service provided by Quanmax or its authorized agent.

■ Maintaining Your Computer

Environmental Factors

■ Temperature

The ambient temperature within an enclosure may be greater than room ambient temperature. Installation in an enclosure should be such that the amount of air flow required for safe operation is not compromised.

Consideration should be given to the maximum rated ambient temperature.

Overheating can cause a variety of problems, including premature aging and failure of chips or mechanical failure of devices.

If the system has been exposed to abnormally cold temperatures, allow a two-hour warm-up period to bring it up to normal operating temperature before turning it on. Failure to do so may cause damage to internal components, particularly the hard disk drive.

■ Humidity

High-humidity can cause moisture to enter and accumulate in the system. This moisture can cause corrosion of internal components and degrade such

properties as electrical resistance and thermal conductivity. Extreme moisture buildup inside the system can result in electrical shorts, which can cause serious damage to the system.

Buildings in which climate is controlled usually maintain an acceptable level of humidity for system equipment. However, if a system is located in an unusually humid location, a dehumidifier can be used to maintain the humidity within an acceptable range. Refer to the “Specifications” section of this user’s guide for the operating and storage humidity specifications.

■ **Altitude**

Operating a system at a high altitude (low pressure) reduces the efficiency of the cooling fans to cool the system. This can cause electrical problems related to arcing and corona effects. This condition can also cause sealed components with internal pressure, such as electrolytic capacitors, to fail or perform at reduced efficiency.

Power Protection

The greatest threats to a system’s supply of power are power loss, power spikes, and power surges caused by electrical storms, which interrupt system operation and/or damage system components. To protect your system, always properly ground power cables and one of the following devices.

■ **Surge Protector**

Surge protectors are available in a variety of types and usually provide a level of protection proportional with the cost of the device. Surge protectors prevent voltage spikes from entering a system through the AC power cord. Surge protectors, however, do not offer protection against brownouts, which occur when the voltage drops more than 20 percent below the normal AC line voltage level.

■ **Line Conditioner**

Line conditioners go beyond the over voltage protection of surge protectors. Line conditioners keep a system’s AC power source voltage at a fairly constant level and, therefore, can handle brownouts. Because of this added protection, line conditioners cost more than surge protectors. However, line conditioners cannot protect against a complete loss of power.

■ **Uninterruptible Power Supply**

Uninterruptible power supply (UPS) systems offer the most complete protection against variations on power because they use battery power to keep the server running when AC power is lost. The battery is charged by the AC power while it is available, so when AC power is lost, the battery can provide power to the system for a limited amount of time, depending on the UPS system.

UPS systems range in price from a few hundred dollars to several thousand dollars, with the more expensive units allowing you to run larger systems for a longer period of time when AC power is lost. UPS systems that provide only 5 minutes of battery power let you conduct an orderly shutdown of the system, but are not intended to provide continued operation. Surge protectors should be used with all UPS systems, and the UPS system should be Underwriters Laboratories (UL) safety approved.

Chapter 1

Introduction

■ Overview

The KEEX-1760 Series is an industrial embedded single board computer (SBC) that equips Intel® BayTrail Processors. Featured are 1x DDR3L SO-DIMM socket, 1x HDMI, 1x VGA, DIO, 2x Gigabit Ethernet, 1x SATA, 1x mini PCIe slot, 4x USB 2.0, 1x USB3.0, 6x COM and Intel® HD Graphics.

Checklist (* : Optional)

- Driver/ Manual CD
- Quick Installation Guide
- KEEX-1760 Series main board
- 1x SATA Cable*
- 1x SATA Power Cable*
- 1x DC Jack Cable*

Features

- Intel® BayTrail Processors
- Intel® HD Graphics
- 1x DDR3L SODIMM up to 4GB
- 1x VGA, 1x HDMI, 2x GbE, 6x COM
- 4x USB 2.0, 1x USB3.0 and Audio
- 1x Mini-Pcie socket, 1x mSATA socket and DIO
- Suitable for Fanless design

■ Product Specifications

Model Name	▪ KEEX-1760 Series
Form Factor	▪ ECX, 146x105mm
CPU Support	<ul style="list-style-type: none"> ▪ KEEX-1760 : Intel® Celeron® Processor J1900(2M Cache, 2.41 GHz) ▪ KEEX-1761 : Intel® Atom™ Processor E3845 (2M Cache, 1.91 GHz) ▪ KEEX-1762 : Intel® Atom™ Processor E3825 (1M Cache, 1.33 GHz)
Memory	▪ 1x DDR3L SO-DIMM up to 4 GB
Graphics	▪ Intel® HD Graphics
Displays	<ul style="list-style-type: none"> ▪ 1x SPWG connector for 24-bit/2-ch LVDS support ▪ 1x Wafer for Panel Power supply ▪ 1x HDMI connector ▪ 1x VGA connector
Ethernet	▪ 2x Gb/s Ethernet ports supported
Audio	<ul style="list-style-type: none"> ▪ Realtek ALC662 Audio Codec onboard 1x header for Line-out/Line-In/MIC-In ▪ ISSI IS31AP2010B 3W Audio Amplifier onboard 2x Wafers for Stereo Speaker output
Peripheral Support	<ul style="list-style-type: none"> ▪ Storage supported <ul style="list-style-type: none"> ● 1x SATA connector ● 1x SATA HDD Power supply ● 1x mSATA socket ▪ USB <ul style="list-style-type: none"> ● 1x USB3.0 port ● 4x USB2.0 ports ▪ FAN supported <ul style="list-style-type: none"> ● +5VDC Smart FAN supported ▪ MISC <ul style="list-style-type: none"> ● 1x header for Reset button, HDD LED and External Speaker support ● 1x header for Power button, Power LED, Keyboard lock and SM bus ● 1x header for mPCIe activity LED support ▪ Super I/O <ul style="list-style-type: none"> ● 6x wafers for RS-232 COM ports ● Pin-1 of Wafers provide +5VDC supply ● COM1 & COM2 support RS-232/422/485 ● 1x wafer for PS/2 KB & MS support ● 8-bit DIO
Expansion Slot	<ul style="list-style-type: none"> ▪ 1x mPCIe Socket ▪ 1x SIM card wafer
BIOS	<ul style="list-style-type: none"> ▪ AMI uEFI BIOS ▪ 1x 16Mb SPI flash ROM
Hardware Monitor	<ul style="list-style-type: none"> ▪ Voltages monitoring ▪ Temperature monitoring.

Watchdog	<ul style="list-style-type: none"> ▪ Programmable WDT to generate System reset event
Real Time Clock	<ul style="list-style-type: none"> ▪ SoC integrated RTC
Power	<ul style="list-style-type: none"> ▪ ACPI 5.0 support ▪ 1x lockable Wafer for +12VDC Power input ▪ ATX/AT Mode Support
Operation Temp.	<ul style="list-style-type: none"> ▪ KEEX-1760 / KEEX-1761 / KEEX-1762 : Operation Temp: 0°C - 60°C Storage Temp.: -20°C - 80°C Humidity: 0% - 95% ▪ KEEX-176A : Operation Temp: -20°C - 60°C Storage Temp.: -20°C - 80°C Humidity: 0% - 95%
Certifications	<ul style="list-style-type: none"> ▪ CE, FCC Class A

Table 1 KEEX-1760 Series Specification

■ System Block Diagram

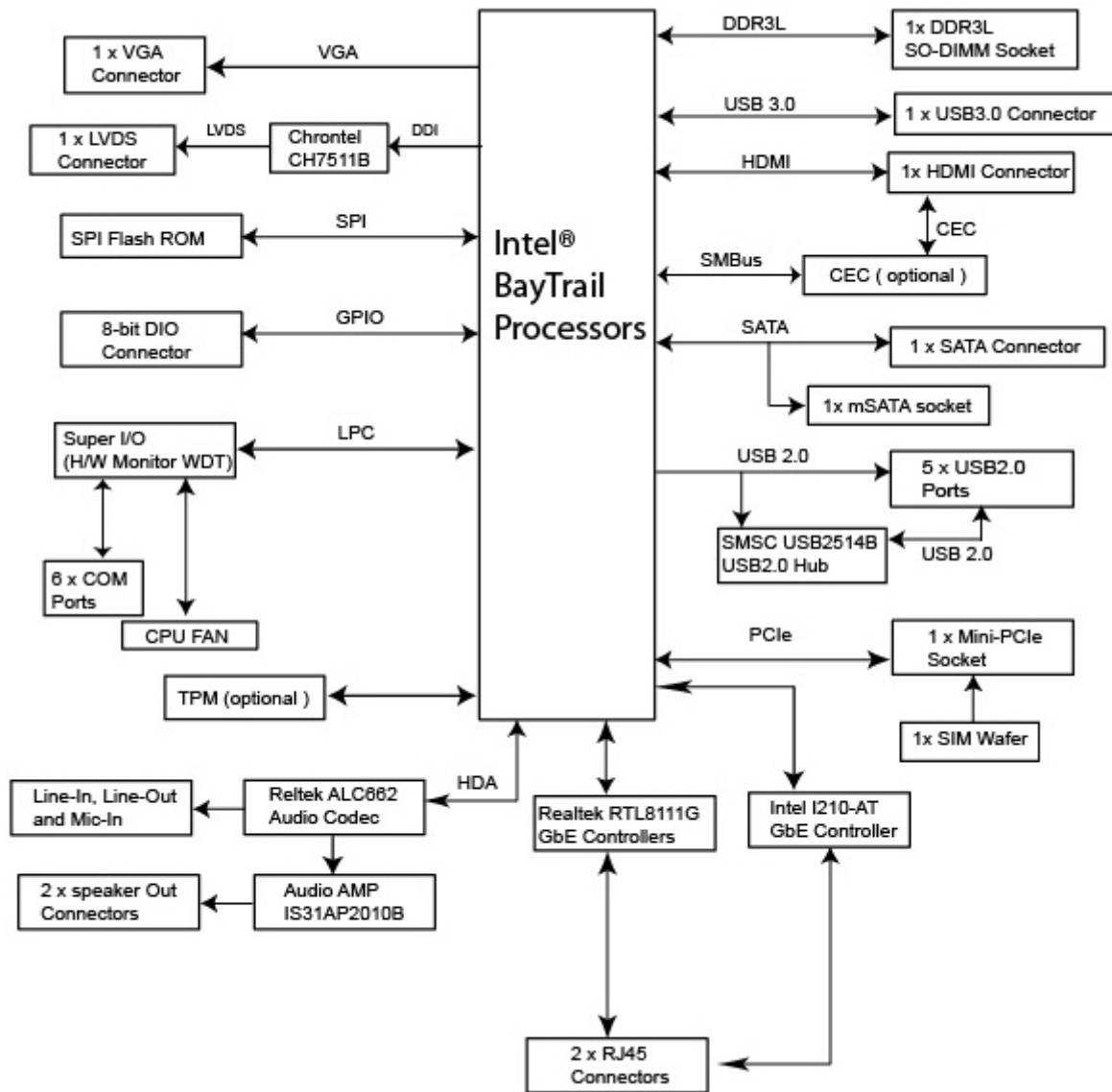


Figure 1 Block Diagram

■ Mechanical Dimensions

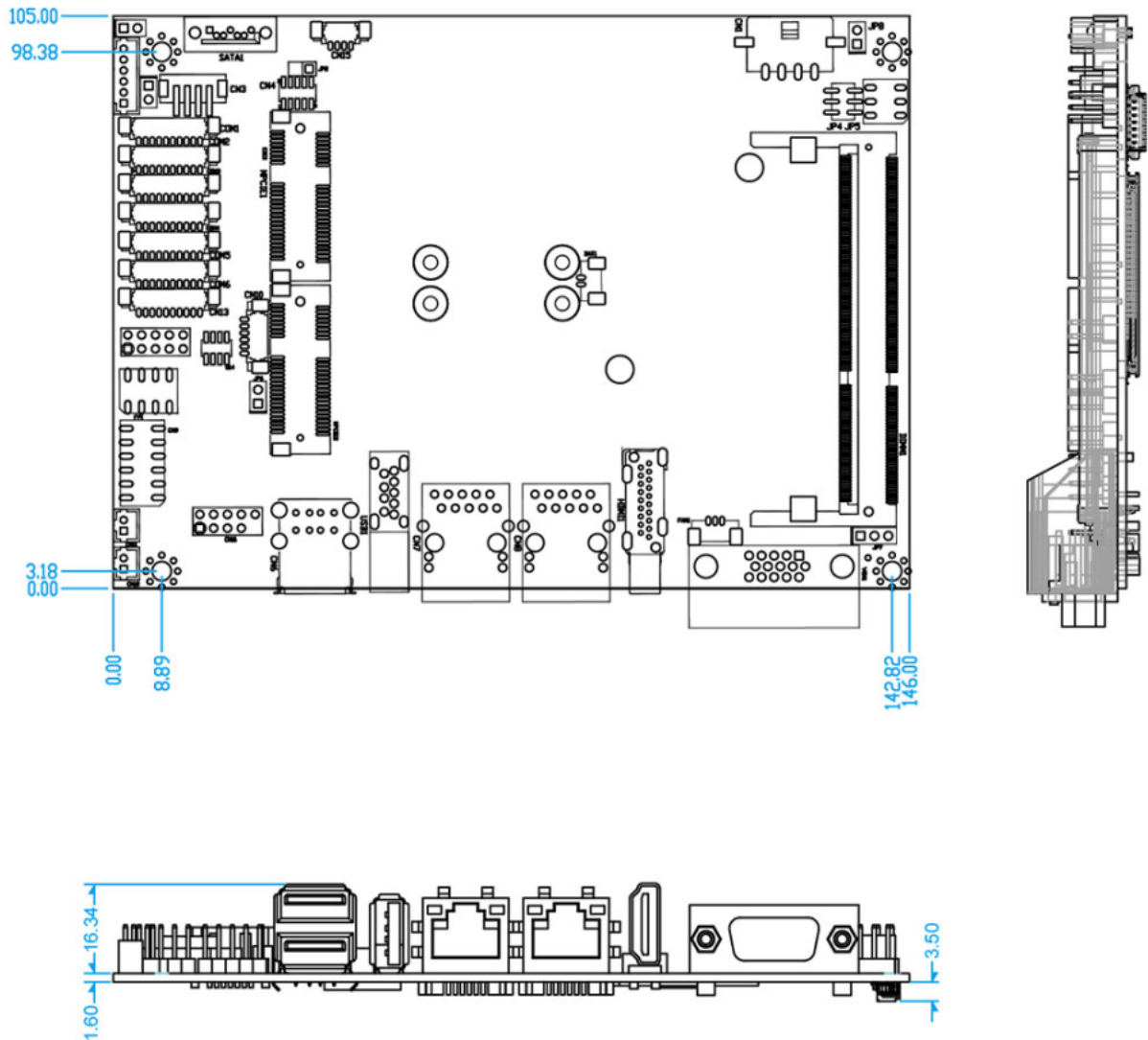


Figure 2 Mechanical Dimensions

Chapter 2

Hardware Settings

■ Overview

This chapter provides the definitions and locations of jumpers, headers, and connectors.

Jumpers

The product has several jumpers which must be properly configured to ensure correct operation.

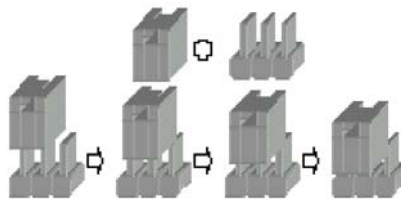


Figure 3 Jumper Connector

For a three-pin jumper (see *Figure 3*), the jumper setting is designated “1-2” when the jumper connects pins 1 and 2. The jumper setting is designated “2-3” when pins 2 and 3 are connected and so on. You will see that one of the lines surrounding a jumper pin is thick, which indicates pin No.1.

To move a jumper from one position to another, use needle-nose pliers or tweezers to pull the pin cap off the pins and move it to the desired position.

■ Jumper Settings and Pin Definitions

For jumper and connector locations, please refer to the diagrams below.

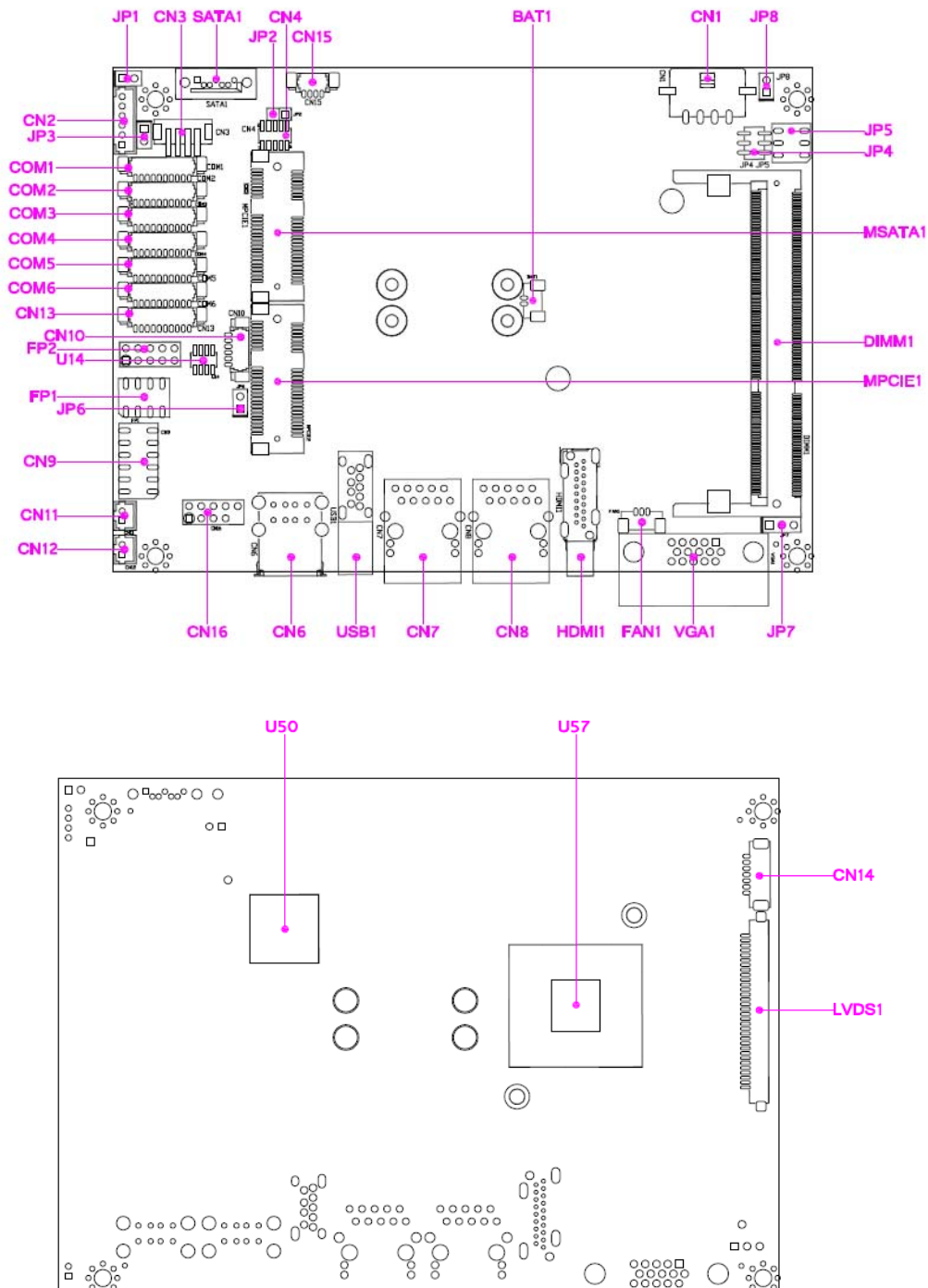


Figure 4 Jumper and Connector Locations

Jumper Settings

To ensure correct system configuration, the following section describes how to set the jumpers to enable/disable or change functions. For jumper descriptions, please refer to the table below.

Table 2 Jumper List

Label	Function
JP1	RTC Reset Selection
JP2	MPCIE Activity LED Indication
JP4	Backlight Enable Selection
JP5	Panel & Backlight Power Selection
JP6	USB Power Selection
JP7	AT / ATX Power Mode Selection
JP8	ME F/W Selection

Table 3 JP1 RTC Reset Selection

Jumper	Status
1-2 Open	Normal Operation.
1-2 Short	Clear CMOS
DIP 2P 1R MALE STRAIGHT TYPE	
Pitch:2.54mm [YIMTEX 3321*02SAGR(6T)]	

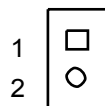


Table 4 JP2 MPCIE Activity LED Indication

Jumper	Status
1	LED+
2	LED-
DIP 2P 1R MALE STRAIGHT TYPE	
Pitch:2.54mm [YIMTEX 3321*02SAGR(6T)]	

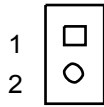


Table 5 JP4 Backlight Enable Selection

Jumper	Setting	Status
1	1-3	Backlight Enable Level = +3.3V
	3-5	Backlight Enable Level = +5V
2	2-4	Backlight Enable High Active
	4-6	Backlight Enable Low Active
SMD 6P 2R MALE TYPE 180D P-2.0mm[PINREX 222-97-03GBB1]		

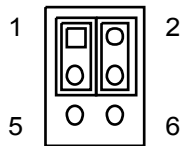


Table 6 JP5 Panel & Backlight Power Selection

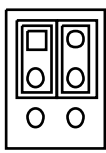
1		2	Jumper	Setting	Status
			1	1-3 3-5	Backlight Power = +12V Backlight Power = +5V
5		6	2	2-4 4-6	Panel Power = +3.3V Panel Power = +5V
			SMD 6P 2R MALE TYPE 180D P-2.0mm[PINREX 222-97-03GBB1]		

Table 7 JP6 USB Power Selection

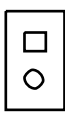
1		Jumper	Status
		1-2 Open	USB power will be cut off in S4 & S5 state.
2		1-2 Short	USB power is always supply.
DIP 2P 1R MALE STRAIGHT TYPE			
Pitch:2.54mm [YIMTEX 3321*02SAGR(6T)]			

Table 8 JP7 AT / ATX Power Mode Selection


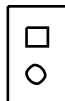
1		Jumper	Status
		1-2 Short	ATX Mode
2		2-3 Short	AT Mode
DIP 3P 1R MALE STRAIGHT TYPE			
Pitch:2.54mm [YIMTEX 3321*02SAGR(6T)]			

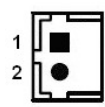
Table 9 JP8 ME F/W Selection

1		Jumper	Status
		1-2 Open	Normal Operation
2		1-2 Short	ME F/W Disabled
DIP 2P 1R MALE STRAIGHT TYPE			
Pitch:2.54mm [YIMTEX 3321*02SAGR(6T)]			

Internal Connector Pin Assignment

Label	Function
BAT1	CR2032 Battery Power Input Wafer
BZ1	Onboard Buzzer
COM1~2	RS-232/422/485 Port Wafer
COM3~6	RS-232 Port 3~6 Wafer
CN1	DC12V Power Input Wafer
CN2	PS/2 Keyboard / Mouse Wafer
CN3	HDD Power Output Wafer
CN4	P80_Header
CN9	Audio Input / Output Pin Header
CN10	SIM Interface Wafer for MPCIE2
CN11	Right Channel 3W Audio AMP Output Wafer
CN12	Left Channel 3W Audio AMP Output Wafer
CN13	Digital Input / Output Wafer
CN14	Backlight Power Output Wafer
DIMM1	DDR3 Memory SO-DIMM Socket
FAN1	DC5V FAN Wafer
FP1	Front Panel 1 Pin Header
FP2	Front Panel 2 Pin Header
LVDS1	18/24-bit, Single Channel LVDS Panel Connector
MPCIE1	Mini-PCI Express v1.2 Socket
MSATA1	Serial ATA Port-1 mSATA Socket
SATA1	Serial ATA Port 0 Connector
CN15	USB2.0 Port 2 Pin Header
CN16	USB2.0 Port USBDN_1,2 Pin Header

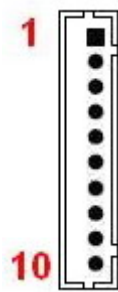
Table 10 BAT1 CR2032 Battery Power Input Wafer



Pin	Signal Name
1	Battery+
2	Battery-

Pitch: 1.25mm [Pinrex 712-73-02TWR0]

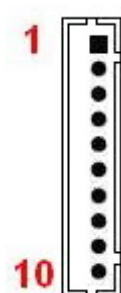
Table 11 COM1~2 RS-232/422/485 Port 1 Wafer



Pin	RS-232	RS-422	Half Duplex RS-485	Full Duplex RS-485
1	DCD	TX-	DATA-	TX-
2	DSR	N/A	N/A	N/A
3	RXD	TX+	DATA+	TX+
4	RTS	N/A	N/A	N/A
5	TXD	RX+	N/A	RX+
6	CTS	N/A	N/A	N/A
7	DTR	RX-	N/A	RX-
8	RI	N/A	N/A	N/A
9	GND	GND	GND	GND
10	+5V	+5V	+5V	+5V

Pitch: 1.25mm [Pinrex 712-73-10TWB0]


Table 12 COM3~6 RS-232 Port 2 Wafer



Pin	Signal Name
1	DCD
2	DSR
3	RXD
4	RTS
5	TXD
6	CTS
7	DTR
8	RI
9	GND
10	+5V

Pitch: 1.25mm [Pinrex 712-73-10TWB0]

Table 13 CN1 DC12V Power Input Wafer



Pin	Signal
1	+12Vin
2	GND
3	GND
4	+12Vin

Pitch: 3.0mm WAFER [Pinrex 733-97-04TBR1]

Table 14 CN2 PS/2 Keyboard / Mouse Wafer



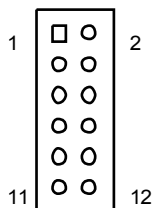
Pin	Signal Name
1	MSCLK
2	VCC
3	MSDAT*
4	KBDAT
5	GND
6	KBCLK
Pitch:2.0mm [YIMTEX 503PW1*06ST-1R]	

Table 15 CN3 HDD Power Output Wafer



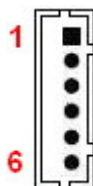
Pin	Signal
1	+12V
2	GND
3	GND
4	+5V
Pitch: 2.0mm WAFER [Pinrex 721-93-04TWE9]	

Table 16 CN9 Audio Input / Output Pin Header



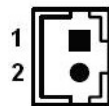
Pin	Signal Name	Pin	Signal Name
1	MIC-In_L	2	MIC-In_R
3	MIC-In_JD#	4	GND
5	Line-In_L	6	Line-In_R
7	Line-In_JD#	8	GND
9	Line-Out_L	10	Line-Out_R
11	Line-Out_JD#	12	GND
Pitch:2.54mm [Pinrex 212-92-06GBE1]			

Table 17 CN10 SIM Interface Wafer for MPCle2



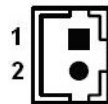
Pin	Signal Name
1	UIM_PWR
2	UIM_DATA
3	UIM_RESET
4	UIM_VPP
5	UIM_CLK
6	GND
Pitch:1.25mm [Pinrex 712-73-06TWB0]	

Table 18 CN11 Right Channel 3W Audio AMP Output Wafer



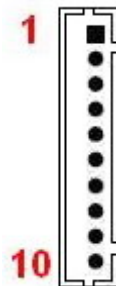
Pin	Signal Name
1	Speaker+
2	Speaker-
Pitch:2.0mm [YIMTEX 503PW1*02STR & 503PW1*02ST-1R]	

Table 19 CN12 Left Channel 3W Audio AMP Output Wafer



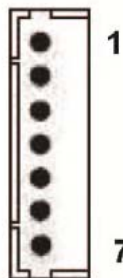
Pin	Signal Name
1	Speaker+
2	Speaker-
Pitch:2.0mm [YIMTEX 503PW1*02STR & 503PW1*02ST-1R]	

Table 20 CN13 Digital Input / Output Wafer



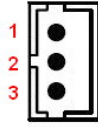
Pin	Signal Name
1	+5V
2	DIO_0
3	DIO_1
4	DIO_2
5	DIO_3
6	DIO_4
7	DIO_5
8	DIO_6
9	DIO_7
10	GND
Pitch:1.25mm [Pinrex 712-73-10TWB0]	

Table 21 CN14 Backlight Power Output Wafer



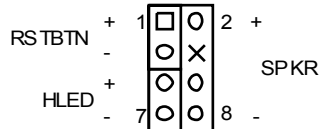
Pin	Signal Name
1	BL_ADJ_PWM *
2	BL_ADJ_VOL *
3	GND
4	+5V / +12V **
5	+5V / +12V **
6	GND
7	BL_EN***
SMD 7P 180D P-1.25mm WAFER [(Townes Enterprise) 1250W-07T1-V]	
* : BL_ADJ can be setting in BIOS setup.	
** : Backlight Power can be selected by JP5.	
*** : BL_EN can be selected by JP4.	

Table 22 FAN1 DC5V FAN Wafer



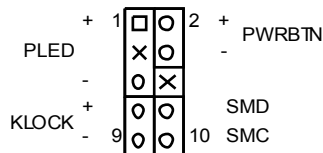
Pin	Signal
1	SENSE
2	+5V
3	GND
Pitch: 1.25mm WAFER [Pinrex 712-73-03TWE0]	

Table 23 FP1 Front Panel 1 Pin Header



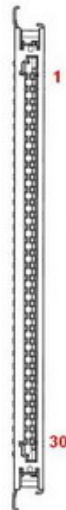
Pin	Signal	Pin	Signal
1	Reset Button +	2	Speaker +
3	Reset Button -	4	NC
5	HDD LED +	6	Internal Speaker-
7	HDD LED -	8	Speaker -
Pitch:2.54mm NY 6T [YIMTEX 3362*04SANGR]			
Note : Internal Buzzer is enabled when Pin6-8 is shorted.			

Table 24 FP2 Front Panel 2 Pin Header



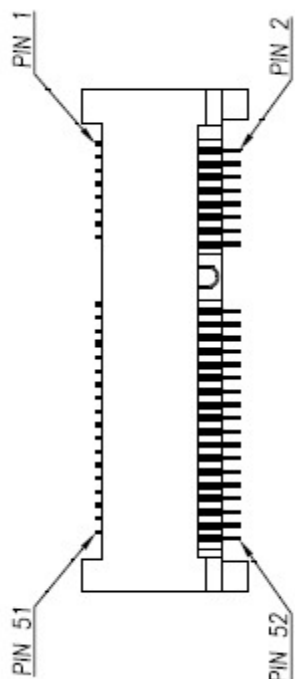
Pin	Signal	Pin	Signal
1	Power LED +	2	Power Button +
3	NC	4	Power Button -
5	Power LED -	6	SMB_ALERT #
7	BATLOW#	8	SMBus Data
9	GND	10	SMBus Clock
Pitch:2.54mm NY 6T[YIMTEX 3362*05SANGR]			

Table 25 LVDS1 Secondary 18/24-bit, 2-Channel LVDS Panel Connector



Pin	Signal Name	Pin	Signal Name
1	LVDS_A0-	16	LVDS_B1+
2	LVDS_A0+	17	GND
3	LVDS_A1-	18	LVDS_B2-
4	LVDS_A1+	19	LVDS_B2+
5	LVDS_A2-	20	LVDSBCLK-
6	LVDS_A2+	21	LVDS_BCLK+
7	GND	22	LVDS_B3-/NC
8	LVDS_ACLK-	23	LVDS_B3+/NC
9	LVDS_ACLK+	24	GND
10	LVDS_A3-/NC	25	DDC_DATA
11	LVDS_A3+/NC	26	VDDEN
12	LVDS_B0-	27	DDC_CLK
13	LVDS_B0+	28	+3.3V / +5V *
14	GND	29	+3.3V / +5V *
15	LVDS_B1-	30	+3.3V / +5V *
SMD MALE 30P 90° 1R Pitch:1mm [JAE JS-1267-30(G1)NH]			
* : Panel Power can be selected by JP5..			

Table 26 MPCle1 Mini-PCI Express v1.2 Socket 1

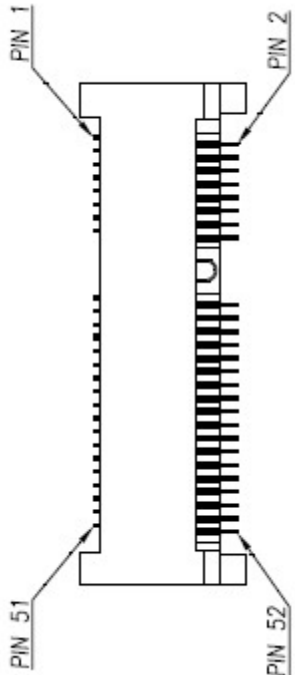


Signal	Pin	Pin	Signal
WAKE#	1	2	+3.3VSB
Reserved	3	4	Ground
Reserved	5	6	+1.5V
CLKREQ#	7	8	UIM_PWR*
Ground	9	10	UIM_DATA*
REFCLK-	11	12	UIM_CLK*
REFCLK+	13	14	UIM_RESET*
Ground	15	16	UIM_VPP*
Reserved	17	18	Ground
Reserved	19	20	W_Disable#
Ground	21	22	PERST#
PERn0	23	24	+3.3VSB
PERp0	25	26	Ground
Ground	27	28	+1.5V
Ground	29	30	SMB_CLK
PETn0	31	32	SMB_DATA
PETp0	33	34	Ground
Ground	35	36	USB_D-
Ground	37	38	USB_D+
+3.3VSB	39	40	Ground
+3.3VSB	41	42	LED_WWAN#
Ground	43	44	LED_WLAN#
Reserved	45	46	LED_WPAN#
Reserved	47	48	+1.5V
Reserved	49	50	Ground
Reserved	51	52	+3.3VSB

H:9.9mm, 10u Gold Plating, Tape Reel [FOXCONN AS0B226-S99Q-7H]

* : These pins are connected to CN10 SIM Interface directly

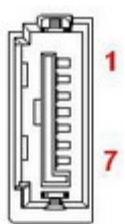
Table 27 MSATA1 Serial ATA Port-1 mSATA Socket



Signal	Pin	Pin	Signal
NC	1	2	+3.3V
NC	3	4	Ground
NC	5	6	NC
N/A	7	8	NC
Ground	9	10	NC
NC	11	12	NC
NC	13	14	NC
Ground	15	16	NC
NC	17	18	Ground
NC	19	20	NC
Ground	21	22	NC
SATA_RX+	23	24	+3.3V
SATA_RX-	25	26	Ground
Ground	27	28	NC
Ground	29	30	NC
SATA_TX-	31	32	NC
SATA_TX+	33	34	Ground
Ground	35	36	NC
Ground	37	38	NC
+3.3V	39	40	Ground
+3.3V	41	42	NC
Ground	43	44	NC
NC	45	46	NC
NC	47	48	NC
NC	49	50	Ground
NC	51	52	+3.3V

H:9.9mm, Au Plating 1u", Black, Tape Reel [LOTES AAA-PCI-093-K01]

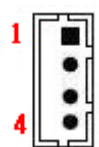
Table 28 SATA1 Serial ATA Port 0 Connector



Pin	Signal Name
1	GND
2	TX+
3	TX-
4	GND
5	RX-
6	RX+
7	GND

[FOXCONN LD1807V-S52U]

Table 29 CN15 USB2.0 Port Wafer



Pin	Signal Name
1	+USBVCC
2	USB_-
3	USB_+
4	GND

SMD 4P MALE 1R P=1.25mm [Pinrex 712-73-04TWE0]

Table 30 CN16 USB2.0 Port 8, 9 Pin Header

1	□	2	
3	○	4	
5	○	6	
7	○	8	
	○	10	

Pin	Signal Name	Pin	Signal Name
1	+USBVCC	2	+USBVCC
3	USB_A-	4	USB_B-
5	USB_A+	6	USB_B+
7	GND	8	GND
9	KEY	10	GND

Pitch:2.54mm [YIMTEX 3362*05SANGR-09]

* : The power source of +USBVCC can be selected by JP6

Rear Panel Pin Assignments

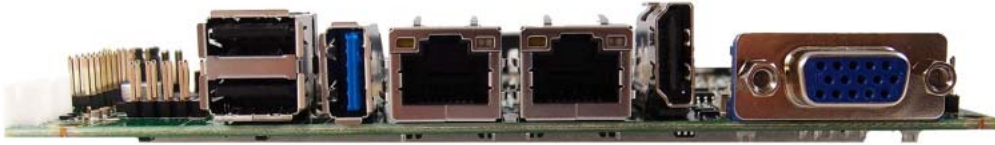
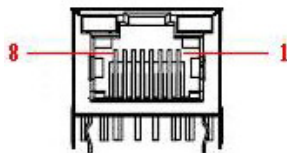


Figure 5 Rear Panel IO

Table 31 Rear Panel Connector List

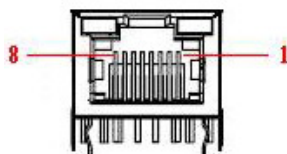
Label	Function
CN7	GbE LAN1 RJ-45 Connector
CN8	GbE LAN2 RJ-45 Connector
CN6	USB2.0 Port 2,3 Type-A Connector
USB1	USB3.0 Port 0 Connector
VGA1	VGA Connector
HDMI1	HDMI Connector

Table 32 CN7 GbE LAN1 RJ-45 Connector



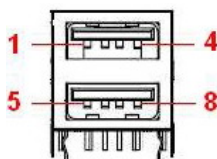
Pin	Signal Name	Pin	Signal Name
1	TX1+	5	TX3-
2	TX1-	6	TX2-
3	TX2+	7	TX4+
4	TX3+	8	TX4-
[UDE RT7-174AAM1A(XA)]			

Table 33 CN8 GbE LAN2 RJ-45 Connector



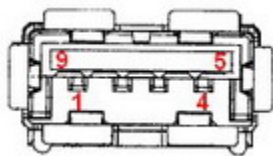
Pin	Signal Name	Pin	Signal Name
1	TX1+	5	TX3-
2	TX1-	6	TX2-
3	TX2+	7	TX4+
4	TX3+	8	TX4-
[UDE RT7-174AAM1A(XA)]			

Table 34 CN6 USB2.0 Port 2,3 Type-A Connector



Pin	Signal Name	Pin	Signal Name
1	+USBVCC	5	+USBVCC
2	USB_A-	6	USB_B-
3	USB_A+	7	USB_B+
4	GND	8	GND
[FOXCONN UB1112C-8FDE-4F]			
* : The power source of +USBVCC can be selected by JP6.			

Table 35 USB1 USB3.0 Port 0 Connector

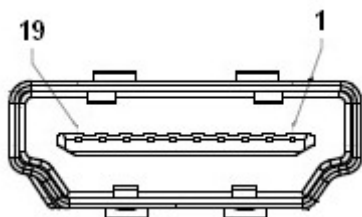


Pin	Signal Name	Pin	Signal Name
1	+USBVCC *	5	USB_RX-
2	USB_D-	6	USB_RX+
3	USB_D+	7	GND
4	GND	8	USB_TX-
		9	USB_TX+

DIP 9P FEMALE, SINGLE UP-RIGHT-T/H type 30u" Pd/Ni + Gold Flash, Tray COLOR:BLUE [FOXCONN UEA3119C-4FB1-4F]

* : The power source of +USBVCC can be selected by JP6.

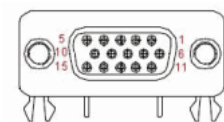
Table 36 HDMI1 HDMI Connector



Pin	Signal
1	TMDS Data2+
2	Ground
3	TMDS Data2-
4	TMDS Data1+
5	Ground
6	TMDS Data1-
7	TMDS Data0+
8	Ground
9	TMDS Data0-
10	TMDS Clock+
11	Ground
12	TMDS Clock-
13	Reserved
14	Reserved
15	DDC_CLK
16	DDC_DATA
17	Ground
18	+5 V Power
19	Hot Plug Detect

HDMI A Side Mount Type, Right Angle, 30u Pd/Ni+Gold Flash, Soft Tray [FOXCONN QJ3119C-WFB1-4F]

Table 37 VGA1 DB-15 VGAConnector



Signal Name	Pin	Pin	Signal Name
Red	1	2	Green
Blue	3	4	NC
GND	5	6	GND
GND	7	8	GND
VCC	9	10	GND
NC	11	12	DDC data
HSYNC	13	14	VSYNC
DDC clock	15		

D-SUB 15P DIP (F) 90D H/D CONNECTOR Blue with hexagonal screws [Kingsun 3125-000-15SB]

Chapter 3

System Installation

■ Memory Module Installation

Carefully follow the steps below in order to install the DIMMs:

1. To avoid generating static electricity and damaging the SO-DIMM, ground yourself by touching a grounded metal surface or use a ground strap before you touch the SO-DIMM.
2. Do not touch the connectors of the SO-DIMM. Dirt or other residue may cause a malfunction.
3. Hold the SO-DIMM with its notch aligned with the memory socket of the board and insert it at a 30-degree angle into the socket.

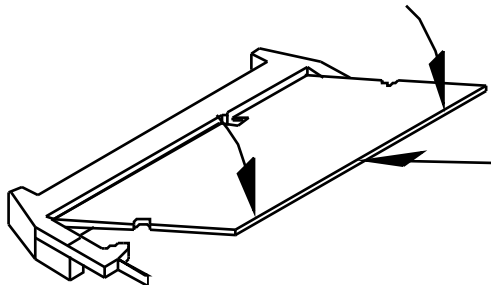


Figure 6 Align the SO-DIMM Memory Module with the onboard socket

4. Fully insert the module into the socket until a “click” is heard.
5. Press down on the SO-DIMM so that the tabs of the socket lock on both sides of the module

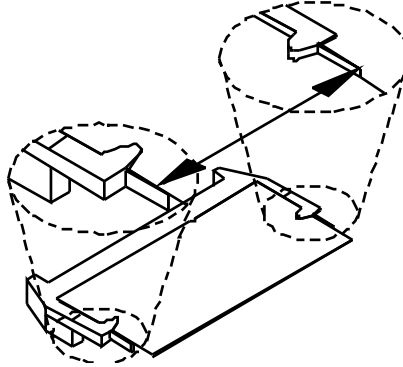


Figure 7 Press down on the SO-DIMM Memory Module to lock it in place

Removing a DIMM:

To remove the SO-DIMM, use your fingers or a small screwdriver to carefully push away the tabs that secure either side of the SO-DIMM. Lift it out of the socket. Make sure you store the SO-DIMM in an anti-static bag. The socket must be populated with memory modules of the same size and manufacturer.

Chapter 4

AMI BIOS Setup

■ Overview

This chapter provides a description of the AMI BIOS. The BIOS setup menus and available selections may vary from those of your product. For specific information on the BIOS for your product, please contact Quanmax.



NOTE: The BIOS menus and selections for your product may vary from those in this chapter. For the BIOS manual specific to your product, please contact Quanmax

AMI's ROM BIOS provides a built-in Setup program, which allows the user to modify the basic system configuration and hardware parameters. The modified data will be stored in a battery-backed CMOS, so that data will be retained even when the power is turned off. In general, the information saved in the CMOS RAM will not need to be changed unless there is a configuration change in the system, such as a hard drive replacement or when a device is added.

It is possible for the CMOS battery to fail, which will cause data loss in the CMOS only. If this happens you will need to reconfigure your BIOS settings.

■ Main Menu

The BIOS Setup is accessed by pressing the DEL key after the Power-On Self-Test (POST) memory test begins and before the operating system boot begins. Once you enter the BIOS Setup Utility, the Main Menu will appear on the screen. The Main Menu provides System Overview information and allows you to set the System Time and Date. Use the “<” and “>” cursor keys to navigate between menu screens.

Table 38 KEEX-1760 BIOS Main Menu

BIOS SETUP UTILITY				
M a i n	A d v a n c e d	B o o t	S e c u r i t y	S a v e & E x i t
Product Information			→ ← Select Screen ↑↓ Select Item Enter: Select +- Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4 Save & Exit ESC Exit	
Product Name		KEEX-1760		
BIOS Version		R0.0A (x64)		
BIOS Build Date		05/26/2014		
ME FW Version		01.01.00.1089		
CPU Infor mati on				
Intel® Celeron® CPU J 1900@ 1.99GHz				
Microcode Revision		809		
Processor Cores		4		
Memor y Infor mati on				
Total Size		4096 MB (DDR3L)		
Frequency		1333 MHz		
System date		[Mon 05/26/2014]		
System time		[13:23:12]		
Access Level		Admi nistrator		
Version 2.16.1242. Copyright (C) 2013, American Megatrends, Inc.				

Table 39 KEEX-1761 BIOS Main Menu

BIOS SETUP UTILITY				
M a i n	A d v a n c e d	B o o t	S e c u r i t y	S a v e & E x i t
Product Information			→ ← Select Screen ↑↓ Select Item Enter: Select +- Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4 Save & Exit ESC Exit	
Product Name		KEEX-1761		
BIOS Version		R0.02 (x64)		
BIOS Build Date		07/217/2014		
ME FW Version		01.00.02.1067		
CPU Information				
Intel® Atom™ BayTrail Quad Core E3845				
Microcode Revision		901		
Processor Cores		4		
Memory Information				
Total Size		2048 MB (DDR3L)		
Frequency		1066 MHz		
System date		[Tur 12/23/2014]		
System time		[13:23:12]		
Access Level		Administrator		
Version 2.16.1242. Copyright (C) 2013, American Megatrends, Inc.				

Table 40 KEEX-1762 BIOS Main Menu

BIOS SETUP UTILITY				
M a i n	A d v a n c e d	B o o t	S e c u r i t y	S a v e & E x i t
Product Information			→ ← Select Screen ↑↓ Select Item Enter: Select +- Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4 Save & Exit ESC Exit	
Product Name		KEEX-1761		
BIOS Version		R0.0A (x64)		
BIOS Build Date		05/26/2014		
ME FW Version		01.01.00.1089		
CPU Information				
Intel® Atom™ BayTrail Dual Core E3825				
Microcode Revision		809		
Processor Cores		4		
Memory Information				
Total Size		4096 MB (DDR3L)		
Frequency		1333 MHz		
System date		[Mon 05/26/2014]		
System time		[13:23:12]		
Access Level		Administrator		
Version 2.16.1242. Copyright (C) 2013, American Megatrends, Inc.				

■ Advanced Menu

Table 41 Advanced Menu

BIOS SETUP UTILITY		
Main	Advanced	Boot Security Server Mgmt Save & Exit
Onboard LAN1 Controller	[Enabled]	→ ← Select Screen ↑↓ Select Item Enter: Select +- Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4 Save & Exit ESC Exit
Onboard LAN1 Boot	[Disabled]	
Onboard LAN2 Controller	[Enabled]	
Onboard LAN2 Boot	[Disabled]	
Audio Controller	[Enabled]	
> Display Configuration		
> Super IO Configuration		
> CPU Chipset Configuration		
> SATA Configuration		
> USB Configuration		
> DIO Configuration		
> H/W Monitor		
Version 2.16.1242. Copyright (C) 2013, American Megatrends, Inc.		

Onboard LAN 1 Controller

Options: Disabled, Enabled

Onboard LAN 1 Boot

Options: Disabled, Enabled

Onboard LAN 2 Controller

Options: Disabled, Enabled

Onboard LAN 2 Boot

Options: Disabled, Enabled

Audio Controller

Options: Disabled, Enabled

Table 42 Advanced Menu – Display Configuration

BIOS SETUP UTILITY		
Main	Advanced	Boot Security Server Mgmt Save & Exit
Display Configuration		→ ← Select Screen ↑↓ Select Item Enter: Select +- Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4 Save & Exit ESC Exit
Primary Display	[Auto]	
UMA Frame Buffer Size	[256 MB]	
DVMT Pre-Allocated	[64M]	
DVMT Total Gfx Mem	[256 M]	
Primary IGFX Boot Display	[VBIOS Default]	
Active LVDS	[Disabled]	
Version 2.16.1242. Copyright (C) 2013, American Megatrends, Inc.		

Primary Display

Options: Auto, IGD

UMA Frame Buffer Size

Options: 128MB, 256MB, 512MB

DVMT Pre-Allocated

Options: 64M, 96M, 128M, 160M, 192M, 224M, 256M, 288M, 320M, 352M, 384M, 416M, 448M, 480M, 512M

DVMT Total Gfx Mem

Options: 128M, 256M, MAX

Primary IGFX Boot Display

Options: VBIOS Default, CRT, HDMI, LVDS

Active LVDS

Options: Disabled, Enabled

LVDS Panel Type

Options: 640x480 18Bit 1CH, 800x480 18Bit 1CH, 800x600 18Bit 1CH, 1024x768 18Bit 1CH, 1440x900 18Bit 1CH, 1600x900 18Bit 1CH

LVDS Backlight Control Mode

Options: Valtage, PWM

LVDS Backlight Control -Voltage

Options: 0.0V, 0.5V, 1.0V, 1.5V, 2.0V, 2.5V, 3.0V, 3.5V, 4.0V, 4.5V, 5.0V

Table 43 Advanced Menu – Super IO Configuration

BIOS SETUP UTILITY	
Main	Advanced
Main Advanced Boot Security Save & Exit	
Super IO Configuration >Serial Port 1 Configuration >Serial Port 2 Configuration >Serial Port 3 Configuration >Serial Port 4 Configuration >Serial Port 5 Configuration >Serial Port 6 Configuration	→ ← Select Screen ↑↓ Select Item Enter: Select +- Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4 Save & Exit ESC Exit
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Table 44 Advanced Menu – Super IO Configuration – Serial Port 1 Configuration

BIOS SETUP UTILITY	
Main	Advanced
Main Advanced Boot Chipset Power Security Exit	
Serial Port 1 Configuration Serial Port [Enabled] Device Settings IO=3F8h ; IRQ=4; Change Settings [Auto] Serial Port 1 Type [RS485] RS485 Duplex Mode [Half Duplex]	→←: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save and Exit ESC: Exit
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Serial Port

Options: Disabled, Enabled

Change Settings

Options: Auto,

IO=3F8h; IRQ=4;

IO=3F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

Serial Port Type

Options: RS232, RS422, RS485

RS485 Duplex Mode

Options: Full Duplex; Half Duplex

Table 45 Advanced Menu – Super IO Configuration – Serial Port 2 Configuration

BIOS SETUP UTILITY				
Main	Advanced	Boot	Security	Save & Exit
Serial Port 2 Configuration				→ ← Select Screen ↑↓ Select Item Enter: Select +- Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4 Save & Exit ESC Exit
Serial Port Device Settings				[Enabled] IO=2F8h; IRQ=3;
Change Settings				[Auto]
Serial Port 2 Type				[RS485]
RS485 Duplex Mode				[Half Duplex]
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Serial Port

Options: Disabled, Enabled

Change Settings

Options: Auto,

IO=2F8h; IRQ=3;

IO=3F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12

Serial Port Type

Options: RS232, RS422, RS485

RS485 Duplex Mode

Options: Full Duplex; Half Duplex

Table 46 Advanced Menu – Super IO Configuration – Serial Port 3 Configuration

BIOS SETUP UTILITY						
Main	Advanced	Boot	Chipset	Power	Security	Exit
Serial Port 3 Configuration					→←: Select Screen	
Serial Port			[Enabled]	↑↓: Select Item		
Device Settings			IO=3E8h ; IRQ=7;	Enter: Select		
Change Settings			[Auto]	+/-: Change Opt.		
				F1: General Help		
				F2: Previous Values		
				F3: Optimized Defaults		
				F4: Save and Exit		
				ESC: Exit		
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Serial Port

Options: Disabled, Enabled

Change Settings

Options: Auto,

IO=3E8h; IRQ=7;

IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

IO=2F0h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

IO=2E0h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

Table 47 Advanced Menu – Super IO Configuration – Serial Port 4 Configuration

BIOS SETUP UTILITY						
Main	Advanced	Boot	Chipset	Power	Security	Exit
Serial Port 4 Configuration					→←: Select Screen	
Serial Port			[Enabled]		↑↓: Select Item	
Device Settings			IO=2E8h ; IRQ=7;		Enter: Select	
Change Settings			[Auto]		+/-: Change Opt.	
					F1: General Help	
					F2: Previous Values	
					F3: Optimized Defaults	
					F4: Save and Exit	
					ESC: Exit	
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Serial Port

Options: Disabled, Enabled

Change Settings

Options: Auto,

IO=2E8h; IRQ=7;

IO=3F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

IO=2F0h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

IO=2E0h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

Table 48 Advanced Menu – Super IO Configuration – Serial Port 5 Configuration

BIOS SETUP UTILITY						
Main	Advanced	Boot	Chipset	Power	Security	Exit
Serial Port 5 Configuration					→←: Select Screen	
Serial Port			[Enabled]	↑↓: Select Item		
Device Settings			IO=2F0h ; IRQ=7;	Enter: Select		
Change Settings			[Auto]	+/-: Change Opt.		
				F1: General Help		
				F2: Previous Values		
				F3: Optimized Defaults		
				F4: Save and Exit		
				ESC: Exit		
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Serial Port

Options: Disabled, Enabled

Change Settings

Options: Auto,

IO=2F0h; IRQ=7;

IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

IO=2F0h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

IO=2E0h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

Table 49 Advanced Menu – Super IO Configuration – Serial Port 6 Configuration

BIOS SETUP UTILITY						
Main	Advanced	Boot	Chipset	Power	Security	Exit
Serial Port 6 Configuration						
Serial Port			[Enabled]			
Device Settings			IO=2E0h ; IRQ=7;			
Change Settings			[Auto]			
Version 2.15.1226. Copyright (C) 2012 American Megatrends, Inc.						

Serial Port

Options: Disabled, Enabled

Change Settings

Options: Auto,

IO=2E0h; IRQ=7;

IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

IO=2F0h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

IO=2E0h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12;

Table 50 Advanced Menu –CPU Advanced Configuration

BIOS SETUP UTILITY				
Main	Advanced	Boot	Security	Save & Exit
CPU Chipset Configuration			→ ← Select Screen ↑↓ Select Item Enter: Select +- Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4 Save & Exit ESC Exit	
EIST		[Enabled]		
Limit CPUID Maximum		[Disabled]		
Execute Disable Bit		[Enabled]		
Intel Virtualization Technology		[Disabled]		
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EIST

Options: Disabled, Enabled

Limit CPUID Maximum

Options: Disabled, Enabled

Execute Disable Bit

Options: Disabled, Enabled

Intel® Virtualization Tech

Options: Disabled, Enabled

Table 51 Advanced Menu –SATA Configuration

BIOS SETUP UTILITY				
Main	Advanced	Boot	Security	Save & Exit
SATA Controller(s)			→ ← Select Screen ↑↓ Select Item Enter: Select +- Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4 Save & Exit ESC Exit	
Serial-ATA (SATA)		[Enabled]		
SATA Mode		[AHCI Mode]		
Serial ATA Port 1		Empty		
Port 1		[Enabled]		
mSATA Port 1		Empty		
Port 1		[Enabled]		
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SATA

Options: Disabled, Enabled

SATA Mode

Options: IDE Mode, AHCI Mode

Port 1

Options: Disabled, Enabled

Table 52 Advanced Menu –USB Configuration

BIOS SETUP UTILITY				
Main	Advanced	Boot	Security	Save & Exit
USB Configuration USB Devices: 1 Keyboard, 2 Hubs LegacyUSB Support [Enabled] xHCI Legacy Support [Enabled] xHCI hand-off [Enabled] EHCI Hand-off [Disabled] USB Mass Storage Driver Support [Enabled] XHCI Mode [Smart Auto]				→ ← Select Screen ↑↓ Select Item Enter: Select +- Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4 Save & Exit ESC Exit
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Legacy USB Support

Options: Disabled, Enabled, Auto

xHCI Legacy Support

Options: Disabled, Enabled

xHCI hand-off

Options: Disabled, Enabled

EHCI hand-off

Options: Disabled, Enabled

USB Mass Storage Driver Support

Options: Disabled, Enabled

XHCI Mode

Options: Smart Auto, Enabled

Table 53 Advanced Menu –DIO Configuration

BIOS SETUP UTILITY				
Main	Advanced	Boot	Security	Save & Exit
DIO Configuration				
DIO-0		[Output Low]		
DIO-1		[Output Low]		
DIO-2		[Output Low]		
DIO-3		[Output Low]		
DIO-4		[Output Low]		
DIO-5		[Output Low]		
DIO-6		[Output Low]		
DIO-7		[Output Low]		
DIO-0 Value		1		
DIO-1 Value		1		
DIO-2 Value		1		
DIO-3 Value		1		
DIO-4 Value		1		
DIO-5 Value		1		
DIO-6 Value		1		
DIO-7 Value		1		
				→ ← Select Screen ↑↓ Select Item Enter: Select +- Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4 Save & Exit ESC Exit
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DIO-0

Options: Output Low, Output High, Input

DIO-1

Options: Output Low, Output High, Input

DIO-2

Options: Output Low, Output High, Input

DIO-3

Options: Output Low, Output High, Input

DIO-4

Options: Output Low, Output High, Input

DIO-5

Options: Output Low, Output High, Input

DIO-6

Options: Output Low, Output High, Input

DIO-7

Options: Output Low, Output High, Input

Table 54 Advanced Menu –H/W Monitor

BIOS SETUP UTILITY		
Main	Advanced	Boot Security Save & Exit
PC Health Status CPU Warning Temperature [Disabled] >CPU FAN Configuration CPU Temperature : +40 C System Temperature : +39 C CPU Fan Speed N/A +VCORE : +0.773 V +VIN : +12.268 V +5V : +5.066 V +VMEN : +1.357 V		→ ← Select Screen ↑↓ Select Item Enter: Select +- Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4 Save & Exit ESC Exit
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CPU Warning Temperature

Options: Disabled, 80, 85, 90, 95

CPU FAN Configuration**FAN Setting [Manual Mode]**

Options: Manual Mode, Auto Mode

Manual Duty 255

Table 55 Power Configuration

BIOS SETUP UTILITY		
Main	Advanced	Boot Security Server Mgmt Save & Exit
Power Management Configuration		
ACPI Sleep State	[S3 (Suspend to RAM)]	
Restore AC Power Loss	[Power Off]	→ ← Select Screen
Power Saving Mode	[Disabled]	↑↓ Select Item
Resume Event control		Enter: Select
Resume From S3 By PS/2 Keyboard	[Disabled]	+ - Change Opt.
Resume From S3 By PS/2 Mouse	[Disabled]	F1: General Help
Resume By PCIE Device	[Disabled]	F2: Previous Values
Resume By Ring Device	[Disabled]	F3: Optimized Defaults
Resume By RTC Alarm	[Disabled]	F4 Save & Exit
		ESC Exit
> Watchdog Timer Configuration		
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ACPI Sleep State

Options: Suspend Disabled, S1 (CPU Stop Clock), S3 (Suspend to RAM)

Restore AC Power Loss

Options: Power Off, Power On, Last State

Resume From S3 By PS/2 Keyboard

Options: Disabled, Enabled

Resume From S3 By PS/2 Mouse

Options: Disabled, Enabled

Resume By PCIE Device

Options: Disabled, Enabled

Resume By RTC Alarm

Options: Disabled, Enabled

EUP Power Saving Mode

Options: Disabled, Enabled

Watchdog Timer Configuration■ **WDT Function** [Disabled]

Options: Disabled, Enabled

■ Boot Menu

Table 56 Boot Menu

BIOS SETUP UTILITY				
Main	Advanced	Boot	Security	Save & Exit
Boot Configuration Full Screen LOGO Display [Disabled] Setup Prompt Timeout 1 Bootup NumLock State [On] Keyboard Detect Warning [Enabled] CSM Support [Enabled] Boot Option Filter [Legacy Only] Boot Option Priorities			→ ← Select Screen ↑↓ Select Item Enter: Select +- Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4 Save & Exit ESC Exit	
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Full Screen LOGO Display

Options: Disabled, Enabled

Bootup Numlock State

Options: On, Off

Keyboard Detect Warning

Options: Disabled, Enabled

CSM Support

Options: Disabled, Enabled

Boot Option Filter

Options: UEFI and Legacy, Legacy only, UEFI only

■ Security Menu

Table 57 Security Menu

BIOS SETUP UTILITY				
Main	Advanced	Boot	Security	Save & Exit
<p>Pass word Description</p> <p>If ONLY the Administrator's password is set, then this only limits access to Setup and is only asked for when entering Setup</p> <p>If ONLY the User's password is set, then this is a power on password and must be entered to boot or enter Setup. In Setup the User will have Administrator rights</p> <p>The pass word length must be in the following range:</p> <p>Minimum Length 3</p> <p>Maximum length 20</p> <p>Administrator Password</p> <p>User Password</p> <p>HDD Security Configuration:</p> <p>HDD 0: WDC WD 1600BE</p>			<p>→ ← Select Screen</p> <p>↑↓ Select Item</p> <p>Enter: Select</p> <p>+ - Change Opt.</p> <p>F1: General Help</p> <p>F2: Previous Values</p> <p>F3: Optimized Defaults</p> <p>F4 Save & Exit</p> <p>ESC Exit</p>	
Version 2.16.1242. Copyright (C) 2013, American Megatrends, Inc.				

■ Save & Exit Menu

Table 58 Save & Exit Menu

BIOS SETUP UTILITY				
Main	Advanced	Boot	Security	Save & Exit
Save Changes and Reset Discard Changes and Reset Save Options Save Changes Discard Changes Restore Defaults			→ ← Select Screen ↑↓ Select Item Enter: Select +- Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4 Save & Exit ESC Exit	
Version 2.16.1242. Copyright (C) 2013, American Megatrends, Inc.				

Save Changes and Exit

Exit system setup after saving the changes. Once you are finished making your selections, choose this option from the Exit menu to ensure the values you selected are saved to the CMOS RAM. The CMOS RAM is sustained by an onboard backup battery and stays on even when the PC is turned off. When you select this option, a confirmation window appears. Select [Yes] to save changes and exit.

Discard Changes and Exit

Exit system setup without saving any changes. Select this option only if you do not want to save the changes that you made to the Setup program. If you made changes to fields other than system date, system time, and password, the BIOS asks for a confirmation before exiting.

Discard Changes

Discards changes done so far to any of the setup values. This option allows you to discard the selections you made and restore the previously saved values. After selecting this option, a confirmation appears. Select [Yes] to discard any changes and load the previously saved values.

Load Optimal Defaults

Load Optimal Default values for all the setup values. This option allows you to load optimal default values for each of the parameters on the Setup menus, which will provide the best performance settings for your system. The F9 key can be used for this operation.

Load Failsafe Defaults

Load Optimal Default values for all the setup values. This option allows you to load failsafe default values for each of the parameters on the Setup menus, which will provide the most stable performance settings. The F8 key can be used for this operation.

Chapter 5

Driver Installation

If your KEEEX-1760 Series does not come with an operating system pre-installed, you will need to install an operating system and the necessary drivers to operate it. After you have finished assembling your system and connected the appropriate power source, power it up using the power supply and install the desired operating system.

You can download the drivers for the KEEEX-1760 Series from the Quanmax website at www.quanmax.com and install as instructed there. For other operating systems, please contact Quanmax.

NOTE



When the system reboots without connecting the CRT, there might be no image on screen when you insert the CRT/VGA cable. Please pressing **<Ctrl>+<Alt>+<F1>** simultaneously to show the image on screen.

Appendix A

DIO (Digital I/O) Sample Code

```

/*****//
//DIO sample code for KEEX-1760 //
//Please compile with Turbo C 3.0 to utilized the program //
// //
//DIO GPIO pin define from SIO IT8783E //
// DIO_0 : SOC S5_GPIO4 DIO_4 : SOC S5_GPIO22 //
// DIO_1 : SOC S5_GPIO5 DIO_5 : SOC S5_GPIO23 //
// DIO_2 : SOC S5_GPIO6 DIO_6 : SOC S5_GPIO24 //
// DIO_3 : SOC S5_GPIO7 DIO_7 : SOC S5_GPIO25 //
// //
/*****//
#include <stdio.h>
#include <conio.h>
#include <dos.h>

typedef unsigned long DWORD;

#define GPIO_BASE 0x500
#define GPIO1_SEL 0x84
#define GPIO1_LVL 0x88

#define SET_IO32(loAddr, dSet) outpd(loAddr, inpd(loAddr) | (dSet))
#define RESET_IO32(loAddr, Rst) outpd(loAddr, inpd(loAddr) &~ (Rst))
#define READ_IO32(loAddr) inpd(loAddr)

DWORD inpd(int portid)
{
    DWORD dwRet;
    asm mov dx, portid;
    asm lea bx, dwRet;

```

```

    __emit__(
    0x66,0x50,          // push EAX
    0x66,0xED,          // in EAX,DX
    0x66,0x89,0x07,    // mov [BX],EAX
    0x66,0x58
    );                  // pop EAX

    return dwRet;
}

void outpd(int portid, DWORD dwVal)
{
    asm mov dx, portid;
    asm lea bx, dwVal;

    __emit__(
    0x66,0x50,          // push EAX
    0x66,0x8B,0x07,     // mov EAX,[BX]
    0x66,0xEF,          // out DX,EAX
    0x66,0x58
    );                  // pop EAX
}

int main()
{
    int gDioGpio[] = {4, 5, 6, 7, 22, 23, 24, 25};
    DWORD gGpioLValue = 0;
    int i = 0;
    //Set DIO_0~7 as Input
    //1:input 0:output
    for (i=0; i<8; ++i)
        SET_IO32(GPIO_BASE+GPIO1_SEL, 0x01 << gDioGpio[i]);

    //Read DIO_0~7 value
    printf("Read DIO_0~7 value\n");
    for (i=0; i< 8; i++)
    {

```

```

        gGpioLValue = READ_IO32(GPIO_BASE + GPIO1_LVL);
        gGpioLValue &= (0x01 << gDioGpio[i]);
        printf("DIO_%d = %d\n",i ,gGpioLValue > 0);
    }
    system("pause");

//Set DIO_0~7 as Output
//0:input 1:output
    for (i=0; i<8; ++i)
    {
        RESET_IO32(GPIO_BASE+GPIO1_SEL, 0x01 << gDioGpio[i]);
    }

//set DIO_0~7 to High
    printf("Set DIO_0~7 to High\n");
    outpd(GPIO_BASE + GPIO1_LVL, 0xFFFFFFFF);
    system("pause");

//set DIO_0~7 to Low
    printf("Set DIO_0~7 to Low\n");
    outpd(GPIO_BASE + GPIO1_LVL, 0x0);
    system("pause");

    return 0;
}

```

Appendix B

WatchDog Timer Sample Code

```
//*****
//*****
//**
**
//**          (C) Copyright 2013-2014, Quanmax, Inc.  **
//**
**
//**          All Rights Reserved.  **
//**
**
//**          5F, No.455, Ti-Ding Blvd., Sec. 2,  **
//**          Neihs District, Taipei, Taiwan 114  **
//**
//**          Phone: +886-2-2799-2789  **
**
//**
**
//*****
//*****
//=====
=====//
//KEEX-1760 DOS Watchdog sample program //
//Please compile with Turbo C 3.0 to utilized the program //
//=====
=====//

#include<stdio.h>

#define SIO_CONFIG_INDEX 0x2e
#define SIO_CONFIG_DATA SIO_CONFIG_INDEX+1

void UnlockSIO()
{
    outp(SIO_CONFIG_INDEX,0x87);
    outp(SIO_CONFIG_INDEX,0x87);
}

void LockSIO()
```

```

{
    outp(SIO_CONFIG_INDEX,0xAA);
}

void SetLDN(int LDN)
{
    outp(SIO_CONFIG_INDEX, 0x07);
    outp(SIO_CONFIG_DATA, LDN);
}

void main()
{
    int value = 0;

    UnlockSIO();
    SetLDN(0x08);

    //Enable WDT
    outp(SIO_CONFIG_INDEX,0x30);
    outp(SIO_CONFIG_DATA,0x01);

    //Set Timer unit(0xF0 bit3(0: 1sec, 1: 60 sec) of watchdog timer by setting
    this bit)
    outp(SIO_CONFIG_INDEX,0xF0);
    value=inp(SIO_CONFIG_DATA);
    outp(SIO_CONFIG_DATA,(value & 0xF7)); //set unit sec.

    //Set Timer Value(0xF1 Time of watchdog timer)
    outp(SIO_CONFIG_INDEX,0xF1);
    outp(SIO_CONFIG_DATA,0x14); //set to 20 sec (0x14)

    LockSIO();
}

```